

ond power supply is to be the master power supply, such that the second power supply sets the voltage on the common share bus, while the first power supply is designated as the slave power supply and the first switch remains in the off position.

3. The power supply system of claim 1, further comprising:

a management controller coupled to the first and second micro-controllers, the management controller selecting the first and second control signals such that the management controller selects the master and slave power supplies.

4. The power supply system of claim 1, further comprising:

a first control circuit coupled between the first micro-controller and a common hardware signal bus, the first control circuit generating the first control signal;

a second control circuit coupled between the second micro-controller and the common hardware signal bus, the second control circuit generating the second control signal; and

wherein the first micro-controller compares a first control signal voltage to a common hardware signal bus voltage in order to determine if the first power supply is the master or slave power supply.

5. The power supply system of claim 4, wherein the first control circuit further comprises:

a first resistor having a first end coupled to ground and a second end coupled to both a first current source and the first micro-controller; and

a diode having a cathode coupled to the first micro-controller and an anode coupled to the common hardware signal bus.

6. The power supply system of claim 1, further comprising:

a first precision rectifier coupled between the first micro-controller and the first switch; and

a second precision rectifier coupled between the second micro-controller and the second switch.

7. The power supply system of claim 6, wherein the first switch comprises one of a gate and a base coupled to the first micro-controller, one of a source and a collector coupled to the first precision rectifier and one of a drain and an emitter coupled to the common share bus.

8. An information handling system (IHS) comprising:

at least one processing node, the processing node having at least one central processing unit (CPU);

a first power supply including a first converter for providing power to the processing node and a first micro-controller coupled to a first switch, the first switch further coupled to a common share bus;

a second power supply including a second converter for providing power to the processing node and a second micro-controller coupled to a second switch, the second switch further coupled to the common share bus; and

wherein the first micro-controller is configured to receive a first control signal designating the first power supply to be either a master power supply or a slave power supply and the second micro-controller is configured to receive a second control signal designating the second power supply to be either the master power supply or the slave power supply.

9. The information handling system of claim 8, wherein the first micro-controller turns on the first switch such that the first power supply sets the voltage on the common share bus.

10. The information handling system of claim 8, further comprising:

a management controller coupled to the first and second micro-controllers, the management controller selecting the first and second control signals such that the management controller selects the master and the slave power supplies.

11. The information handling system of claim 8, further comprising:

a first control circuit coupled between the first micro-controller and a common hardware signal bus, the first control circuit generating the first control signal;

a second control circuit coupled between the second micro-controller and the common hardware signal bus, the second control circuit generating the second control signal; and

wherein the first micro-controller compares a first control signal voltage to a common hardware signal bus voltage in order to determine if the first power supply is the master or slave power supply.

12. The information handling system of claim 8, wherein the first control circuit further comprises:

a first resistor having a first end coupled to ground and a second end coupled to both a first current source and the first micro-controller, the first resistor and the first current source determining the first control signal; and

a diode having a cathode coupled to the first micro-controller and an anode coupled to the common hardware signal bus.

13. The information handling system of claim 8, further comprising:

a first precision rectifier coupled between the first micro-controller and the first switch; and

a second precision rectifier coupled between the second micro-controller and the second switch.

14. The information handling system of claim 8, wherein the first switch comprises one of a gate and a base coupled to the first micro-controller, one of a source and a collector coupled to the first precision rectifier and one of a drain and an emitter coupled to the common share bus.

15. A method of operating a power supply system, the method comprising:

receiving a first control signal at a first micro-controller of a first power supply;

determining if the first control signal identifies the first power supply as a master power supply;

in response to determining that the first control signal has identified the first power supply as the master power supply, turning on a first switch coupled between the first micro-controller and a common share bus such that the first power supply is configured as a master power supply that sets the voltage on the common share bus.

16. The method of claim 15, further comprising:

in response to determining that the first control signal has not identified the first power supply as the master power supply, turning off the first switch coupled between the first micro-controller and the common share bus such that the first power supply is configured as a slave power supply.